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# Thermal

### Steve reported at the UCL meeting

- First simulations done
- Layout was close to current design

#### Now updated

- □ Simulations with realistic design
- Gut feeling "that heat quickly gets into the tungsten confirmed by calculation
- Estimate about 13°C temperature change along slab
   Need to check this with the latest pad size layout
  - Points to note
    - □ Keep the air gaps as small as possible
    - Conduction improved if the chips can be put in better contact with tungsten

05/05/2006

# Thermal

Will need to make measurements to verify simulations
 Propose to build cooling test rig in Manchester

Measure real temperature profiles

Provide a test bed for implementing active cooling systems if we find it necessary

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### Glue

- Environmental chamber is now up and running again
  - Moving up to the new labs slowed things down somewhat
  - First results from thermal cycling coming out
- Still have no silicon from
   Czech people to do tests on
   despite repeated requests
  - Chasing that now, but probably easier to contact them in Montreal if they're there



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